Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	7337	circuit same design same specification	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 09:50
L5	74	map$4 adj20 (technolog$3 near9 level)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 10:48
L8	22	l3 and l5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 10:52
L9	6469	layout same technolog$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 10:52
L10	16	l8 and l9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 10:52
L11	15	l10 and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 10:58
L12	2	map$4 and "6378123".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 11:01
L13	1	l12 and cell same transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 11:01
L14	1	map$4 same cell and "6378123". pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/31 11:02